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In re Application of: :
Kshirsagar, *et al* :
Application No.: **10/568,842** :
PCT No.: PCT/DE2004/001799 :
Int. Filing Date: 11 August 2004 :
Priority Date: 20 August 2003 :
Atty. Docket No.: :
I431.151.101/FIN504PCT/US :
For: Electrical Diagnostic Circuit and :
Method for the Testing and/or :
Diagnostic Analysis of an Integrated :
Circuit :

STATUS REQUEST

Dear Mr. Gleason:

This letter is in response to your communication received 16 November 2007, requesting the status of the Filing Receipt for U.S. national application **10/568,842**.

The United States Designated Office/Elected Office (DO/EO) has docketed the U.S. national application for preparation of the Filing Receipt. However, the USPTO is currently experiencing a backlog of applications awaiting examination. The filing receipt should issue in due course. The appropriate personnel of the Designated Office/Elected Office (DO/EO) to which this application has been assigned have been notified of your request.

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